

## Features

- Much lower Ron\*A performance for On-state efficiency
- Better efficiency due to very low FOM

## Applications

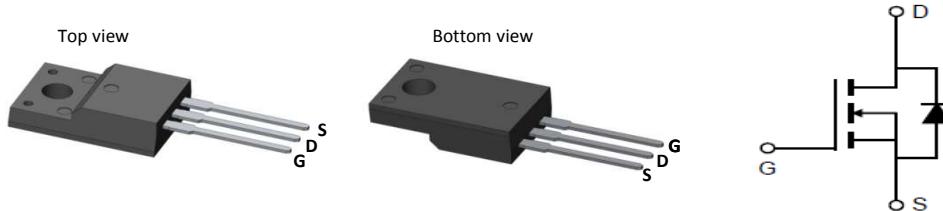
- LED/LCD/PDP TV and monitor Lighting
- Solar/Renewable/UPS-Micro Inverter System
- Charger
- Power Supply

## Product Summary

VDS	650V
R <sub>DS(on)</sub> _typ	0.39Ω
I <sub>D</sub>	11A

**100% DVDS Tested**

**100% Avalanche Tested**



## Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
MCJF390N65GC	JF390N65GC	TO220F	Tube	N/A	N/A	50pcs

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V <sub>DS</sub>	650	V
Continuous drain current T <sub>C</sub> = 25°C T <sub>C</sub> = 100°C	I <sub>D</sub>	11 7.0	A
Pulsed drain current (T <sub>C</sub> = 25°C, t <sub>p</sub> limited by T <sub>jmax</sub> )	I <sub>D</sub> pulse	44	A
Avalanche energy, single pulse (L=60mH, R <sub>g</sub> =30Ω)	E <sub>AS</sub>	120	mJ
MOSFET dv/dt ruggedness	dv/dt	50	V/ns
Gate-Source voltage	V <sub>GS</sub>	±30	V
Power dissipation (T <sub>C</sub> = 25°C)	P <sub>tot</sub>	29	W
Continuous diode forward current(T <sub>C</sub> = 25°C)	I <sub>S</sub>	11	A
Diode pulse current <sup>2)</sup> (T <sub>C</sub> = 25°C)	I <sub>S</sub> pulse	44	A
Recovery diode dv/dt <sup>3)</sup>	dv/dt	50	V/ns
Operating junction and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55...+150	°C

1) Limited by T<sub>j,max</sub>. Maximum Duty Cycle D = 0.50; TO-220 equivalent

2) Pulse width t<sub>p</sub> limited by T<sub>j,max</sub>

3) Identical low side and high side switch with identical RG

**Thermal Resistance**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case	R <sub>thJC</sub>	-	3.07	4.30	°C/W	
Thermal resistance, junction – ambient	R <sub>thJA</sub>	-	-	78	°C/W	

**Electrical Characteristic (at T<sub>j</sub> = 25 °C, unless otherwise specified)**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

**Static Characteristic**

Drain-source breakdown voltage	BV <sub>DSS</sub>	650	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA
Gate threshold voltage	V <sub>GS(th)</sub>	3.5	-	4.5	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V
		-	10	-		T <sub>j</sub> =25°C
Gate-source leakage current	I <sub>GSS</sub>	-	0.3	80	nA	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V
Drain-source on-state resistance	R <sub>DS(on)</sub>	-	0.39	0.45	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =5.5A,
		-	1	-		T <sub>j</sub> =25°C
Transconductance	g <sub>f</sub>	-	12	-	S	V <sub>DS</sub> =20V, I <sub>D</sub> =5.5A

**Dynamic Characteristic**

Input Capacitance	C <sub>iss</sub>	-	790	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =100V, f=1MHz
Output Capacitance	C <sub>oss</sub>	-	32	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	2	-		
Gate Total Charge	Q <sub>G</sub>	-	22	-	nC	V <sub>GS</sub> =10V, V <sub>DS</sub> =480V, I <sub>D</sub> =5.5A
Gate-Source charge	Q <sub>gs</sub>	-	5.3	-		
Gate-Drain charge	Q <sub>gd</sub>	-	8.8	-		
Turn-on delay time	t <sub>d(on)</sub>	-	20	-		
Rise time	t <sub>r</sub>	-	15	-		
Turn-off delay time	t <sub>d(off)</sub>	-	74	-	ns	T <sub>j</sub> =25°C, V <sub>GS</sub> =10V, I <sub>D</sub> =5.5A, V <sub>DS</sub> =400V, R <sub>g</sub> =25Ω
Fall time	t <sub>f</sub>	-	43	-		
Gate resistance	R <sub>G</sub>	-	2.0	-		

### Body Diode Characteristic

<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>			<b>Unit</b>	<b>Test Condition</b>
		<b>min.</b>	<b>typ.</b>	<b>max.</b>		
Body Diode Forward Voltage	$V_{SD}$	0.5	0.84	1	V	$V_{GS}=0V, I_{SD}=5.5A$
Body Diode Reverse Recovery Time	$t_{rr}$	-	218	-	ns	$I_{sd}=5.5A$ $dI/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	$Q_{rr}$	-	2.35	-	$\mu C$	$V_{ds}=100V$

## Typical Performance Characteristics

Fig 1. Output Characteristics ( $T_j=25^\circ\text{C}$ )

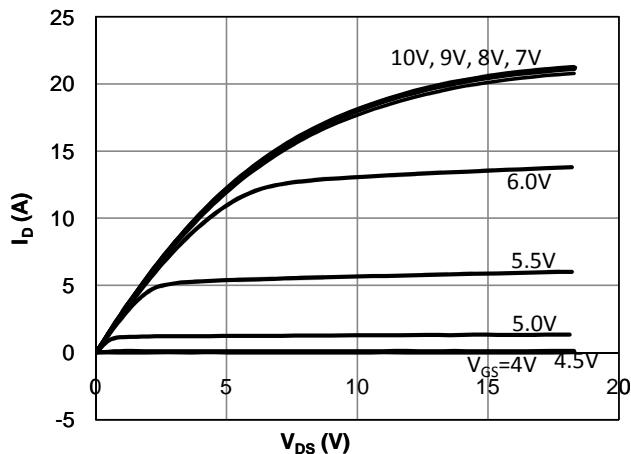


Fig 2. Output Characteristics ( $T_j=150^\circ\text{C}$ )

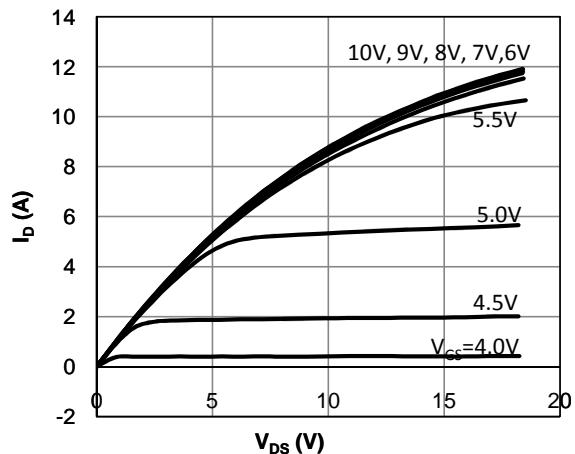


Fig 3: Transfer Characteristics

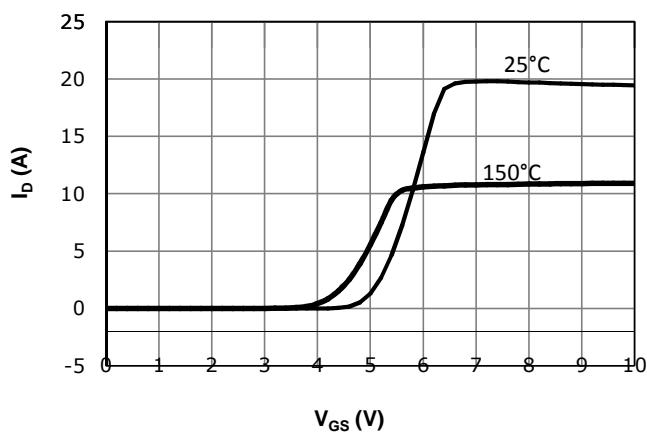


Fig 4:  $V_{TH}$  Vs  $T_j$  Temperature Characteristics

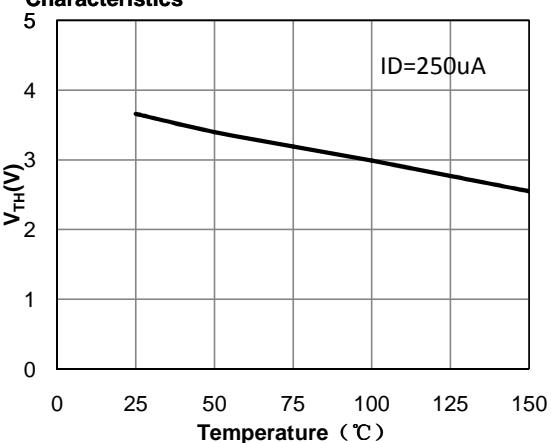


Fig 5:  $R_{DSON}$  Vs  $I_{DS}$  Characteristics( $T_c=25^\circ\text{C}$ )

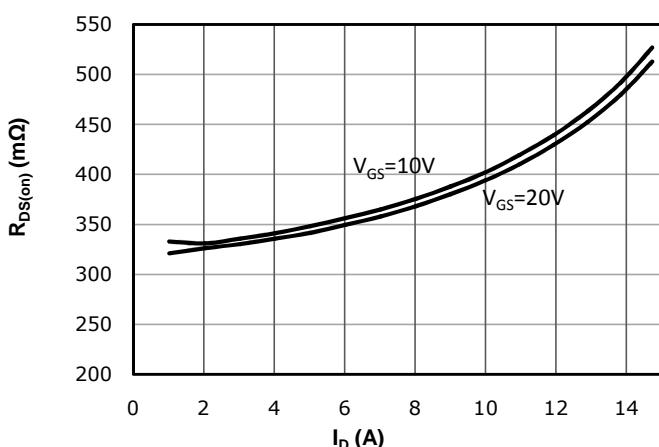


Fig 6:  $R_{D(on)}$  vs. Temperature

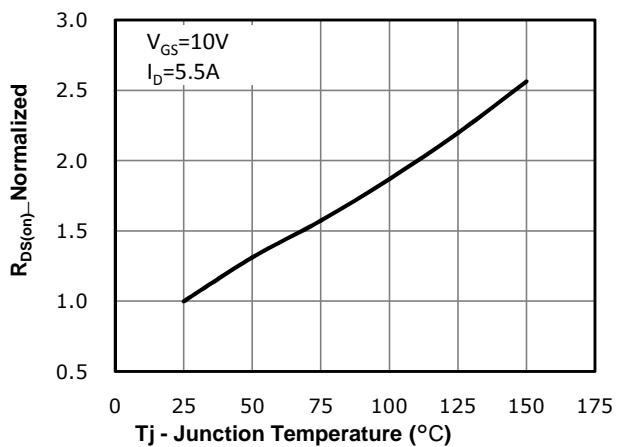


Fig 7: BV<sub>DSS</sub> vs. Temperature

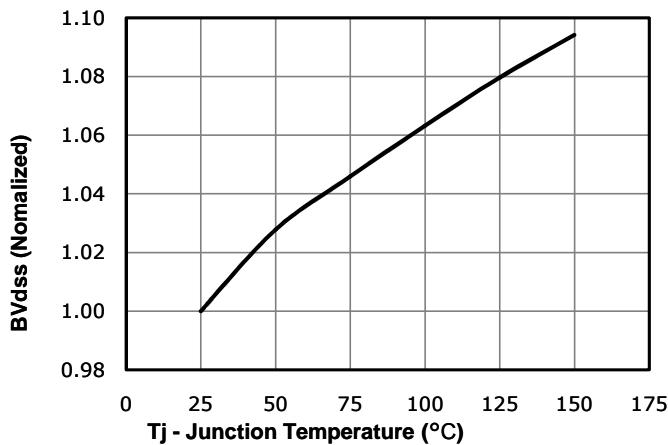


Fig 8: R<sub>d(on)</sub> vs Gate Voltage

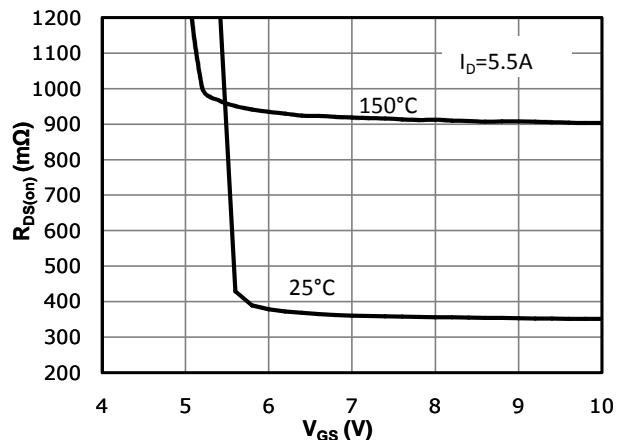


Fig 9: Body-diode Forward Characteristics

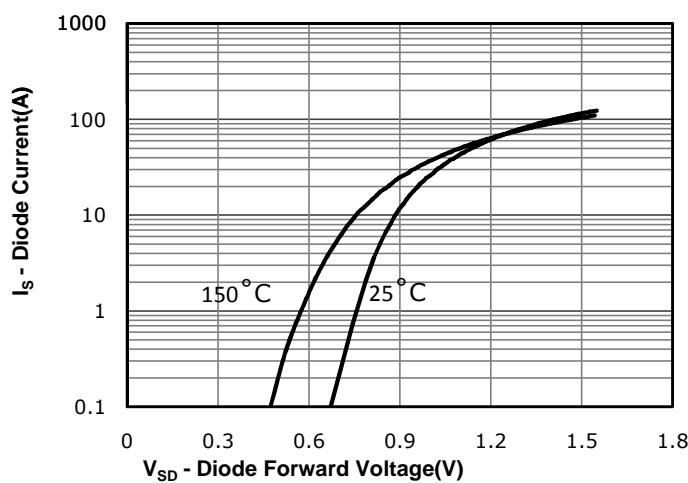


Fig 10: Gate Charge Characteristics

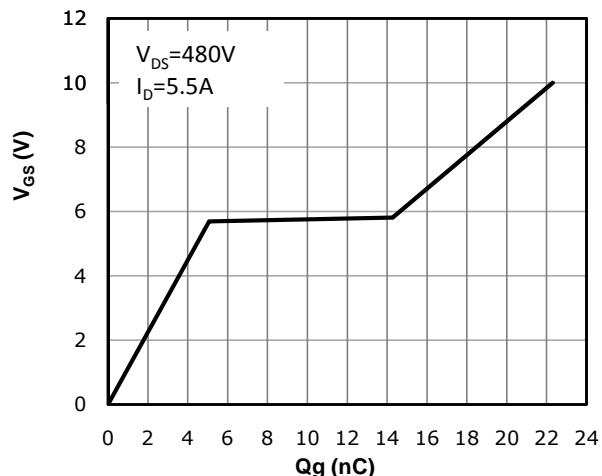


Fig 11: Capacitance Characteristics

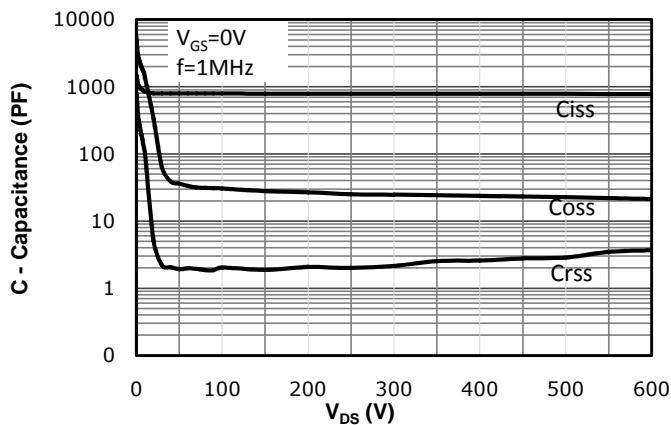


Fig 12: Safe Operating Area

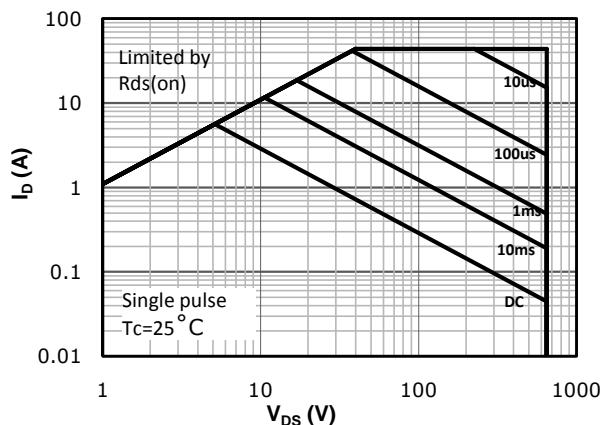
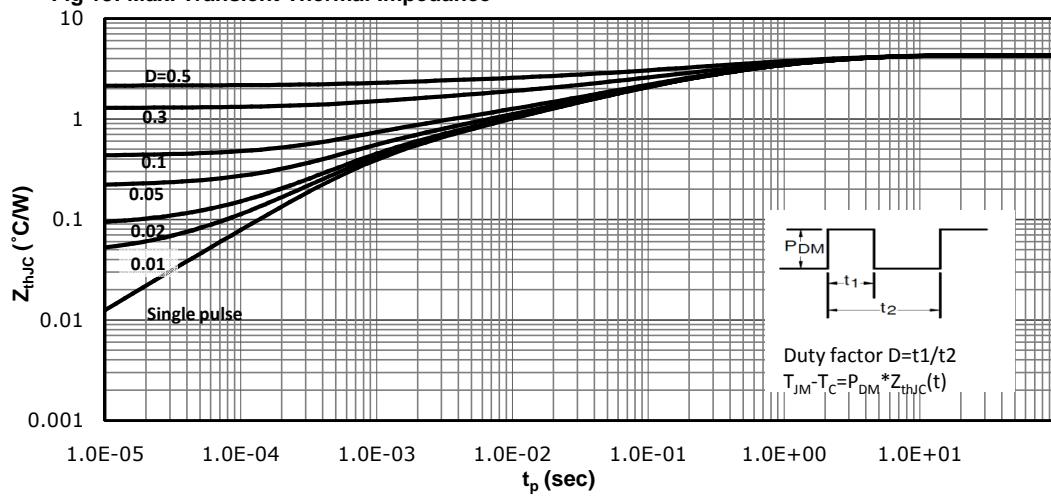
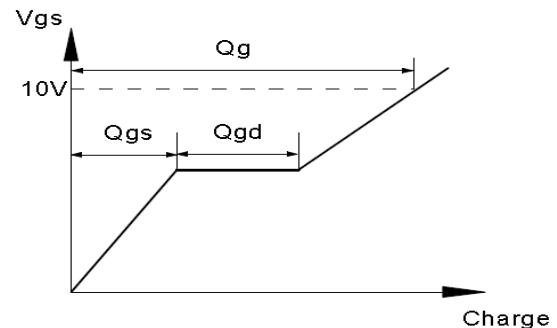
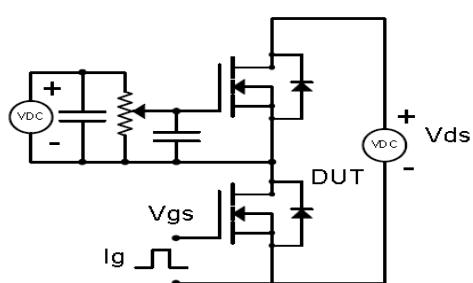


Fig 13: Max. Transient Thermal Impedance

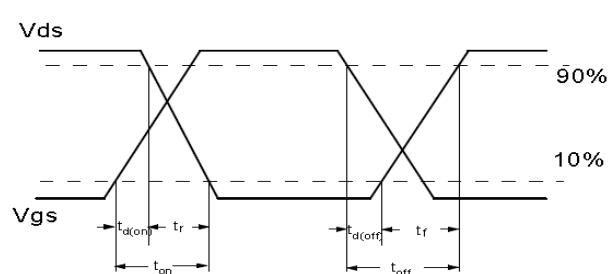
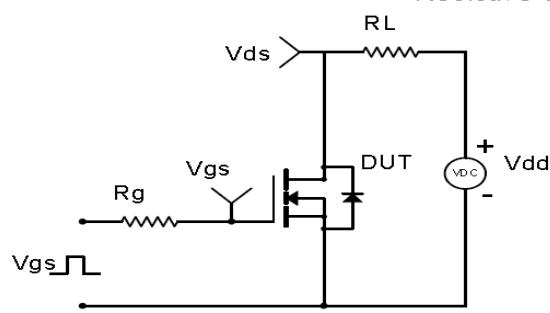


## Test Circuit & Waveform

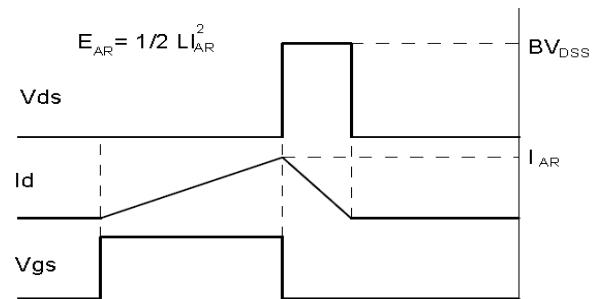
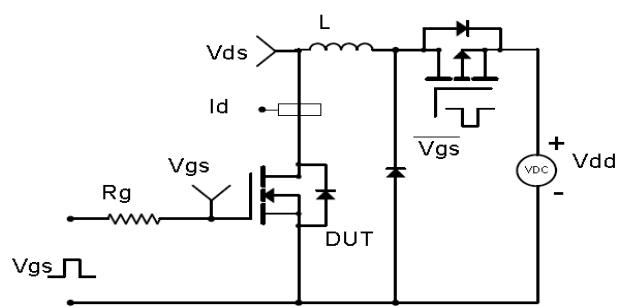
Gate Charge Test Circuit & Waveform



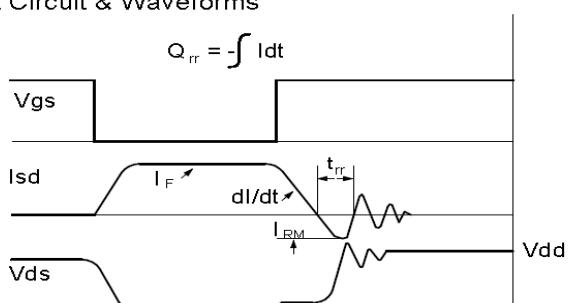
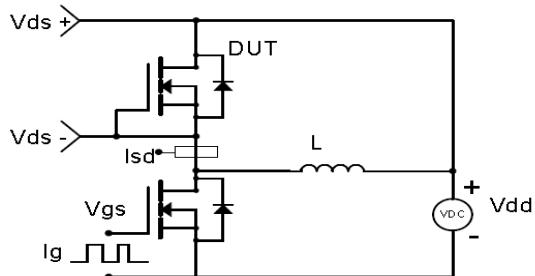
Resistive Switching Test Circuit & Waveforms



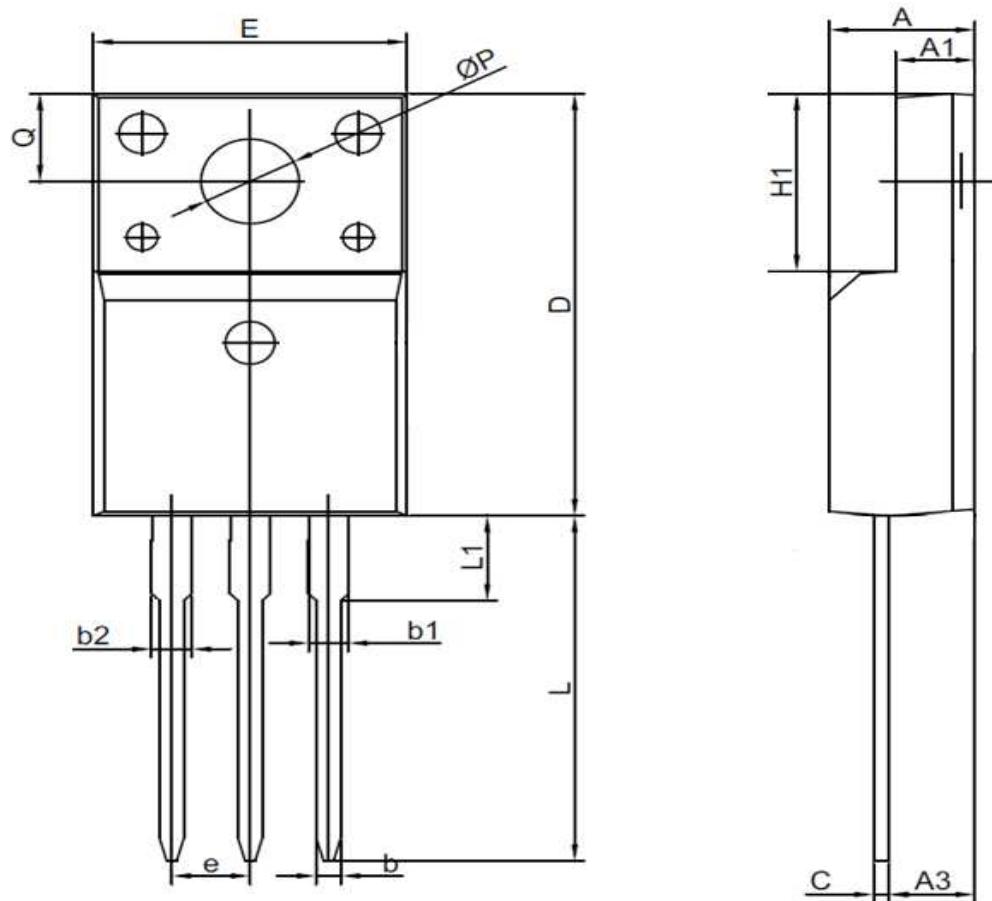
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



**Package Outline: TO-220F**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.30	4.90	0.169	0.193
A1	2.34	2.87	0.092	0.113
A3	2.20	2.96	0.087	0.117
b	0.60	0.90	0.024	0.035
b1	0.95	1.45	0.037	0.057
b2	1.15	1.55	0.045	0.061
c	0.40	0.70	0.016	0.028
D	15.50	16.17	0.610	0.637
e	2.54 BSC		0.100 BSC	
E	9.70	10.66	0.382	0.420
H1	6.70 REF		0.264 REF	
L	12.46	13.75	0.491	0.541
L1	2.80	3.80	0.110	0.150
Q	3.05	3.55	0.120	0.140
P	2.98	3.38	0.117	0.133

**Marking**

NOTE:  
NXBAAAAY  
X —Assembly location code  
BB —Fab code  
AAAA —Lot code  
Y —Bin code

**Disclaimer**

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.